

## CLAIMS

1. A reference voltage generation circuit for generating a constant reference voltage at a reference voltage output terminal, comprising:

5 a first diode element having a cathode connected to a ground potential;

a second diode element which has a current density different from that of the first diode element and whose cathode is connected to the ground potential;

a first resistive element having an end connected to an anode of the second diode element;

10 a second resistive element having an end connected to the other end of the first resistive element, the other end of the second resistive element being connected to the reference voltage output terminal;

a third resistive element having an end connected to the anode of the first diode element and the other end connected to the reference voltage output terminal;

15 a first P-type transistor for supplying a current to the reference voltage output terminal;

a second P-type transistor having a gate terminal connected to its own drain terminal and to a gate terminal of the first P-type transistor;

a bandgap reference circuit having a feedback type control circuit for controlling a  
20 drain current of the second P-type transistor such that a voltage at the anode of the first diode element is equal to a voltage at a connection point between the first and second resistive elements; and

a start-up circuit for, if an output voltage of the reference voltage output terminal of the bandgap reference circuit is at an abnormal stabilization point, shifting the output  
25 voltage from the abnormal stabilization point to a normal stabilization point,

wherein the start-up circuit is provided between the drain terminal of the second P-type transistor of the bandgap reference circuit and the ground potential and, if the drain

current of the second P-type transistor is substantially zero, increases the drain current of the second P-type transistor.

2. The reference voltage generation circuit of claim 1, wherein the start-up circuit is a P-type transistor whose gate terminal is connected to the reference voltage output terminal.

3. The reference voltage generation circuit of claim 1, wherein the start-up circuit includes:

a P-type transistor having a gate terminal connected to the reference voltage output terminal; and

a current generating element provided between a source terminal of the P-type transistor and a drain terminal of the second P-type transistor of the bandgap reference circuit.

4. The reference voltage generation circuit of claim 3, wherein the current generating element is a resistive element.

5. The reference voltage generation circuit of claim 3, wherein the current generating element is a diode element.

6. The reference voltage generation circuit of claim 3, wherein the current generating element is a transistor whose gate terminal is connected to a drain terminal.

7. The reference voltage generation circuit of claim 3, wherein the current generating element is a transistor whose gate terminal is fixed to a constant voltage.